



U.S. Patent Application No.: 10/716,596
Attorney Docket No.: 57941.000063
Client Reference No.: RA001.2003.2.C.US

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: :
Michael Farmwald et al. : Group Art Unit: 2111
Appln. No.: 10/716,596 : Examiner: Glenn Allen Auve
Filed: November 20, 2003 : Confirmation No.: 7201
For: INTEGRATED CIRCUIT I/O USING A: Customer No.: 38013
HIGH PERFORMANCE BUS INTERFACE:

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicants hereby submit attached Form PTO-1449 (modified) listing cited references. This submission is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

Per the voice communication between the Examiner and the undersigned on September 29, 2006, the filing of this supplemental information disclosure statement was necessitated by the filing of Requests for Inter Partes Reexamination for

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related U.S. Patent Nos. 6,426,916 and 6,324,120. The Examiner may note that many of the listed references are dated after the effective filing date of the above-identified patent application. The Requests for Inter Partes Reexamination allege that such references are relevant because the claims presented in the respective patents are not entitled to their effective filing dates. Many of the other references and arguments presented in the Requests for Inter Partes Reexamination were considered in U.S. District Court and U.S. Appellate Court patent litigation proceedings involving related patents. Such proceedings, which included claim construction proceedings for the related patents, include Rambus Inc. v. Infineon Technologies AG et al., No. 3:00cv524 (E.D. Vir. 2001), Rambus Inc. v. Infineon Technologies AG et al., 318 F.3d 1081 (Fed. Cir. 2003), and Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N.D. Cal. 2005). The Hynix case specifically included the patents for which reexamination has been requested, and many of the arguments and references relied upon in the Requests for Inter Partes Reexamination were presented in those proceedings. (See e.g., reference citation nos. 17 and 31-33). If the Examiner believes that additional information relating to such proceedings would be helpful in determining patentability of the above-identified patent

application, Applicants would gladly provide same.

Applicants hereby expressly reserve the right to swear behind the effective dates of any of the cited references. Applicants also reserve the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to the filing of this supplemental information disclosure statement. Further, the filing of this supplemental information disclosure statement is not to be construed as a representation that a search has, or has not, been conducted or that no better art exists.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicants.

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may

be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. Any such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(c), this information disclosure statement is being filed (i) before the mailing date of a final Office Action for the above-identified patent application; (ii) before the mailing date of a Notice of Allowance for the above-identified patent application; or (iii) before the mailing date of an Office Action that otherwise closes prosecution in the above-identified patent application. Accordingly, this information disclosure statement is accompanied by a check or an authorization to charge the information disclosure statement submission fee of \$180.00 under 37 C.F.R. § 1.17(p) to Deposit Account No. 50-0206.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

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Attorney Docket No.: 57941.000063
Client Reference No.: RA001.2003.2.C.US

Respectfully submitted,

Hunton & Williams LLP

By: 

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TEA/asc

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Date: October 2, 2006

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number	10/716,596
Filing Date	November 20, 2003
First Named Inventor	Michael Farmwald
Art Unit	2111
Examiner Name	Glenn Allen Auve
Attorney Docket Number	57941.000063

Sheet 1 of 5

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DATE CONSIDERED

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Application Number

10/716,596

Filing Date

November 20, 2003

First Named Inventor

Michael Farmwald

Art Unit

2111

Examiner Name

Glenn Allen Auve

Sheet

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Attorney Docket Number

57941.000063

FOREIGN PATENT DOCUMENTS

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Substitute for form 1449/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;"><i>(use as many sheets as necessary)</i></p>		Application Number 10/716,596	Filing Date November 20, 2003
		First Named Inventor Michael Farmwald	Art Unit 2111
		Examiner Name Glenn Allen Auve	
		Attorney Docket Number 57941.000063	
Sheet	3 of 5		

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	4.	Hyundai, HY5DV651622, Rev. 0.9, published January 2000 ("Hynix-1")	<input type="checkbox"/>	<input type="checkbox"/>
	5.	Hyundai, SDRAM Timing Diagram, Rev.1.2, published December 1999 ("Hynix-2")	<input type="checkbox"/>	<input type="checkbox"/>
	6.	Hyundai, DDR SDRAM Device Operation, Rev. 0.2, published December 1998 ("Hynix-3")	<input type="checkbox"/>	<input type="checkbox"/>
	7.	Claim Chart for Claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 40-41 based on Park, with additional references to the JEDEC Standard	<input type="checkbox"/>	<input type="checkbox"/>
	8.	Claim Chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard	<input type="checkbox"/>	<input type="checkbox"/>
	9.	Claim Chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover	<input type="checkbox"/>	<input type="checkbox"/>
	10.	Claim Chart for claims 1-41 of the '916 patent based on the Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover	<input type="checkbox"/>	<input type="checkbox"/>
	11.	Patent Family Tree Chart	<input type="checkbox"/>	<input type="checkbox"/>
	12.	File History of the '916 patent	<input type="checkbox"/>	<input type="checkbox"/>
	13.	File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history)	<input type="checkbox"/>	<input type="checkbox"/>
	14.	Rambus Inc. v. Samsung Electronics Co., Ltd., et al., No. C05 02298 RMW (N. D. Cal. 2006), Preliminary Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>

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Sheet	4 of 5	Attorney Docket Number	57941.000063

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			YES	NO
	15.	Samsung Electronics Co., Ltd., v. Rambus Inc., No. 3:05cv406 (E. D. Va. 2006), Memorandum Opinion filed July 18, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	16.	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005), Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3	<input type="checkbox"/>	<input type="checkbox"/>
	17.	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005), Claim Construction Order filed Nov. 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	18.	Selected Excerpts from the Trial Transcript from Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005)	<input type="checkbox"/>	<input type="checkbox"/>
	19.	Rambus Inc. v. Infineon Technologies AG et al., No. 3:00cv524 (E. D. Vir. 2001), Memorandum Opinion	<input type="checkbox"/>	<input type="checkbox"/>
	20.	Rambus Inc. v. Infineon Technologies AG et al., 318 F.3d 1081 (Fed. Cir. 2003)	<input type="checkbox"/>	<input type="checkbox"/>
	21.	Waters, "Hynix Told to Pay Damages to Rambus", Financial Times, April 24, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	22.	Poletti, "Rambus wins victory from Supreme Court - Win could Lead to Success for Rambus' Other Lawsuits seeking Royalties", San Jose Mercury News, October 7, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	23.	Kanellos, "Future of memory market hangs on Rambus trials - It's the trial of the century, at least as far as the memory industry is concerned, CNET News.com, February 12, 2001, http://news.com.com/Future+of+memory+market+hangs+on+Rambus+trials/2100-1001_3-252404.html	<input type="checkbox"/>	<input type="checkbox"/>
	24.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Opinion of the Commission	<input type="checkbox"/>	<input type="checkbox"/>
	25.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Concurring Opinion of Commission Jon Leibowitz	<input type="checkbox"/>	<input type="checkbox"/>

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Sheet	5 of 5	Attorney Docket Number	57941.000063

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			YES	NO
	26.	In the Matter of Rambus, Inc., FTC Docket No. 9302 Order Reversing and Vacating Initial Decision...	<input type="checkbox"/>	<input type="checkbox"/>
	27.	Rambus' Final Infringement Contentions in the Hynix Litigation	<input type="checkbox"/>	<input type="checkbox"/>
	28.	Exhibit A (accused products) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	29.	Exhibit P ('916 infringement chart) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	30.	Exhibit R (Exemplary data sheet) to Rambus' Final Infringement Contentions	<input type="checkbox"/>	<input type="checkbox"/>
	31.	Request for <i>Inter Partes</i> Reexamination under 35 U.S.C. §§ 311-318 for U.S. Patent No. 6,324,120.	<input type="checkbox"/>	<input type="checkbox"/>
	32.	Request for <i>Inter Partes</i> Reexamination under 35 U.S.C. §§ 311-318 for U.S. Patent No. 6,426,916.	<input type="checkbox"/>	<input type="checkbox"/>
	33.	Special Verdict Form filed April 24, 2006, in Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005)	<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
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